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12/19/2001

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10037485	FILING DATE 12/19/2001	CLASS <del>436</del> 267	SUBCLASS +55	GAU 2811	EXAMINER H. VU
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\*\*APPLICANTS: Mastromatteo Ubaldo;

\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 00830867.8 12/29/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☐ yes ☐ no

35 USC 119 conditions met

☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

854063.665

TITLE : Process for manufacturing integrated devices having connections on a separate wafer, and integrated device thus obtained

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>
<b>ISSUE FEE</b>		<b>DRAWING</b>
Amount Due	Date Paid	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>
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